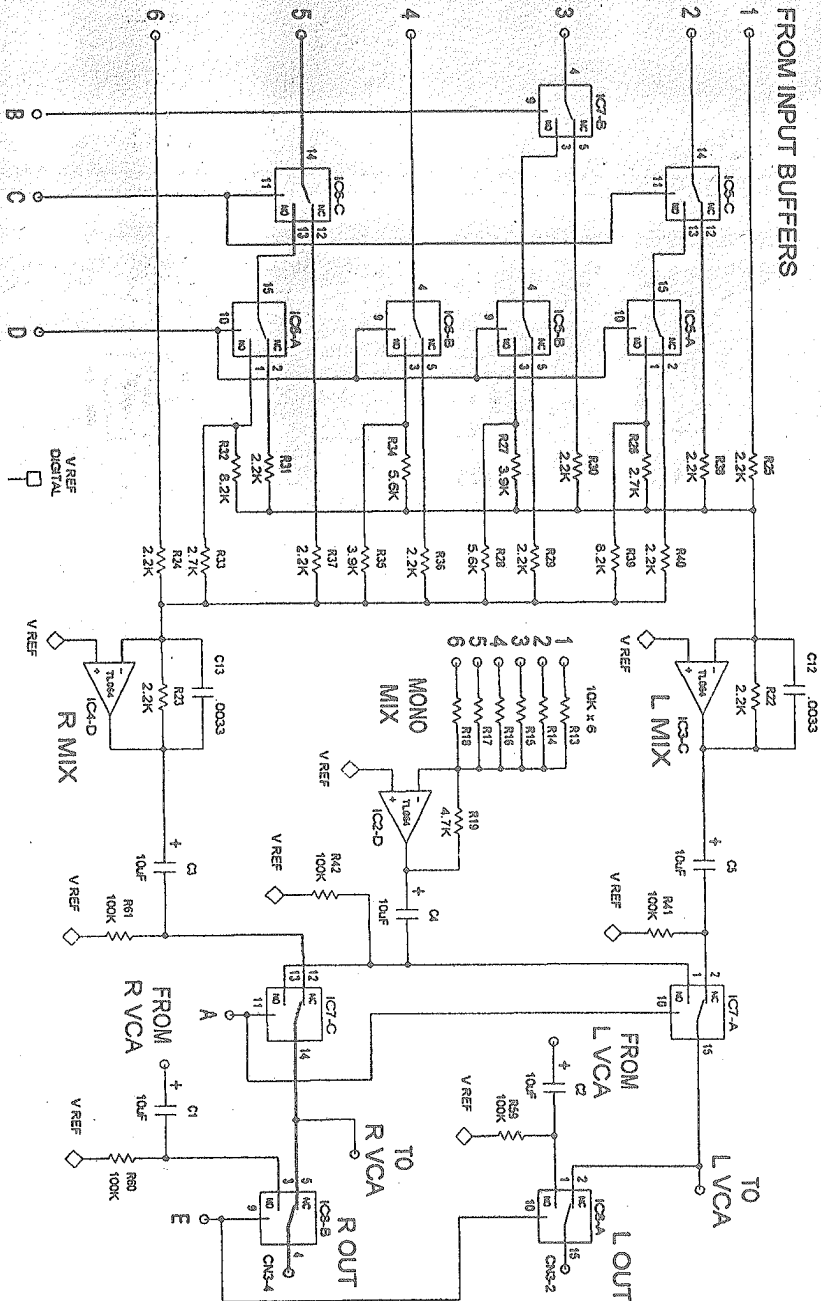


FROM INPUT BUFFERS



FROM DIODE MATRIX

IC 5, 6, 7, 8 = 74HC4053
PIN 16 TO V₊
PIN 7 TO GND
PINS 6 AND 8 TO V REF DIGITAL

REVISIONS

LTR	ECN	DESCRIPTION	REV

PART NO.	DESCRIPTION
UNLESS OTHERWISE SPECIFIED	
RESISTORS	1% TOLERANCE
CAPACITORS	5% TOLERANCE
IC'S	74HC4053
DIODES	1N4148
RELAY	7718/95
DRWING	MM
CHECKED	b.h.
APPROVED	
SCALE	1:1
SHEET	2 OF 2
REV.	1
DATE	42-2248
FILE	hex_02.dwg

HEX BUFFERS AND LOGIC SCHEMATIC REV 1.0

SCALE 1:1